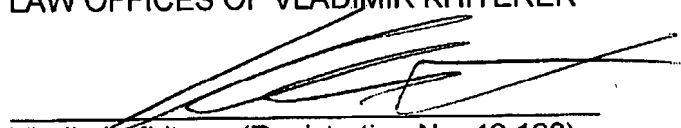


noted that the cited reference is a simulation which does not account for this fact. In a real physical world, this will degrade the source signal even before it is evaluated for degradation by the circuit. (In contrast, the present invention is based on avoiding introduction any additional degradation to the source signal due to evaluation for degradation).

5. For the foregoing reasons, Applicant respectfully requests that claims 1-4 be allowed.

DATED: 10-10-00 LAW OFFICES OF VLADIMIR KHITERER

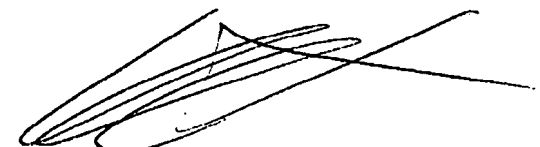

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